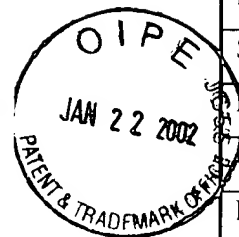


67/2814



First Named Inventor	Durcan et al.	TRANSMITTAL FORM UNDER 37 CFR 1.8 (LARGE ENTITY)
Serial No.	09/652,998	
Filing Date	August 31, 2000	
Group Art Unit	2814	
Examiner Name	A. Mai	
Attorney Docket No.	400.153US07	
Title: CONTAINER CAPACITOR STRUCTURE AND METHOD OF FORMATION THEREOF		

Commissioner for Patents
Washington, DC 20231

Enclosures								
The following documents are enclosed:								
<input checked="" type="checkbox"/>	An Amendment and Response to Office Action (5 pgs., plus Marked-Up Version of Amendments 2 pgs.)							
<input checked="" type="checkbox"/>	A return postcard.							
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.								
<u>CUSTOMER NUMBER 27073</u>								
Fee Calculation								
	Number of Claims		Prv. Amt.	Extra Claims		Fee		Fee Paid
Total Claims	4	-20	2	0	X	\$18	=	\$0
Independent Claims	3	-3	1	0	X	\$84	=	\$0
							=	
Total								\$0
Submitted By								
Name	Russell D. Slifer	Reg. No.	39,838	Telephone	(612) 312-2202			
Signature				Date	December 21, 2001			
Certificate of Mailing								
I certify that this correspondence, and the documents identified above, are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on <u>December 21, 2001</u> .								
Name	Susan Donovan			Signature				



S/N 09/652,998

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Durcan et al.

Examiner: A. Mai

Serial No.: 09/652,998

Group Art Unit: 2814

Filed: August 31, 2000

Docket: 400.153US07

Title: CONTAINER CAPACITOR STRUCTURE AND METHOD OF FORMATION
THEREOF

#8/B
2/12/2
Jules

AMENDMENT AND RESPONSE

Commissioner for Patents
Washington, D.C. 20231

In response to the Office Action dated September 24, 2001, please amend the above-identified patent application as follows:

IN THE CLAIMS

Please amend the claims as follows:

13. An array of capacitors, comprising:

a first bottom capacitor plate;

a second bottom capacitor plate;

a third bottom capacitor plate;

a contact between said first bottom capacitor plate and said second bottom capacitor plate, the contact downwardly extends from a vertical height above a top of the first bottom capacitor plate;

a trench between said second bottom capacitor plate and said third bottom capacitor plate;

a common top capacitor plate over said first bottom capacitor plate, said second bottom capacitor plate, and said third bottom capacitor plate, wherein said top capacitor plate extends toward said contact at a first level within said array, the top capacitor plate includes a lateral clearance opening at the first level around the contact, and wherein said top capacitor plate lines a side of said trench and further lines a bottom of said trench at a second level within said array;

and

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